

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

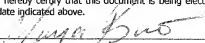
In re Applicant:	§	Art Unit:	2482
Louis Lippincott et al.	§		
	§	Examiner:	Chikaodili E. Anyikire
Serial No.:	§		
10/750,075	§	Conf. No.:	2083
	§		
Filed:	§	Docket:	ITL1703US
December 31, 2003	§		P17498
	§		
For:	§	Assignee:	Intel Corporation
Motion Estimation Sum of all	§		
Differences (SAD) Array Having	§		
Reduced Semiconductor Die	§		
Area Consumption	§		

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed January 19, 2011, please amend the above-referenced patent application as follows:

Date of Deposit: March 8, 2011
I hereby certify that this document is being electronically transmitted on the date indicated above.

Mayra Bravo